

# U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10006334	FILING DATE 12/03/2001	CLASS 438	SUBCLASS 674	GAU 2818	EXAMINER Q. Hoang
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\*\*APPLICANTS: Gadepally Kamesh;

\*\*CONTINUING DATA VERIFIED:

THIS APPLICATION IS A CIP OF 09/430,348 10/29/1999 PAT 6,329,287

\*\*FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO NSC1-G0610 [P04402 P01]
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		
Verified and Acknowledged Examiners's initials		

TITLE : Method for manufacturing an integrated circuit structure with limited source salicidation

U.S. DEPT. OF COMM./PAT. & TM.-PTO-4361 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claims Ver. 0.05
Assistant Examiner		DRAWING	
Amount Due		Sheets Drawg.	Fig. Drawg.
Date Paid		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		Application Examiner	
PREPARED FOR ISSUE		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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